



Bhattacharya 1-4-2-2-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): R. Bhattacharya et al.

Case: 1-4-2-2-1

Serial No.: 10/620,045

Filing Date: July 15, 2003

Group: 2123

Examiner: To Be Assigned

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: December 16, 2003

Signature: Laura M. Hanke

Title: Method and Apparatus for Automatic Generation
of Multiple Integrated Circuit Simulation Configuration

TRANSMITTAL OF MISSING PARTS

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Notice to File Missing Parts dated October 16, 2003, Applicants submit herewith the following documents to complete the above-referenced patent application:

- (1) Assignment documents (3) with Recordation Form Cover Sheet;
- (2) Declaration and Power of Attorney documents (3); and
- (3) Information Disclosure Statement with Form PTO-1449 and cited reference(s).

A copy of the Notice is enclosed herewith. Please charge **Ryan, Mason & Lewis, LLP** **Deposit Account No. 50-0762** the amount of \$130, to cover the surcharge under 37 CFR §1.16(e). A duplicate copy of this letter is enclosed. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,

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Date: December 16, 2003